UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,807,036 B2

Page 1 of 7

APPLICATION NO.: 10/087125

DATED

: October 19, 2004

INVENTOR(S) : John R. Baldwin

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The Title Page showing an illustrative Fig. should be deleted and substitute therefore the attached title page.

For Figs. 1-5, please substitute the attached drawing sheets which label terminals 26 and 28 as load terminals.

Signed and Sealed this

Twenty-sixth Day of December, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office

(12) United States Patent Baldwin

(10) Patent No.: (45) Date of Patent: US 6,807,036 B2 Oct. 19, 2004

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Subject to any disclaimer, the term of this patent is extended or adjusted under 35 Notice:

U.S.C. 154(b) by 131 days.

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(51)	Int. Cl. ⁷	Н02Н 3/00
(52)	U.S. Cl	361/42
	Dield of Connels	261/42_50 03.1

Field of Search 361/93.2, 93.4, 93.5, 93.6, 71-73

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ABSTRACT

A ground fault interrupter apparatus is provided that employs a switching device that is operated in two stages. The first stage operates a solenoid in order to close the contacts and preferably provides coupling between the load and the power source. The second stage maintains the coupling, however, drawing less current and power from the internal devices of the apparatus. Accordingly, the fault interrupter meets various standards for the time required to open and close the contacts as well as keeping the dissipated beat from the device within the required range.

24 Claims, 5 Drawing Sheets











